Text: *The Art of Electronics* (2nd Ed) by P. Horowitz and W. Hill (optional)

Topics:
- Capacitors
- Diode Circuits
- BiPolar Transistors
- FETs
- Op amps
- Oscillators
- Logic Gates
- Flip-Flops
- Counters
- Memory; States
- Analog-to-Digital Conversion; Phase-Locked Loops
- Interfacing to computers (embedded devices)

Tests: The testing schedule will be flexible, with 2 or 3 exams throughout the semester. There will be no final exam.

Homework: Homework problems must be worked prior to the class in which they are due.

Grades: One half of the course grade will come from the lab book grades. The remainder will come from the exams and homework. All labs must be completed by the end of the last week of classes.

Individual Help: Any student who wishes individual help is urged to see the instructor. As there will be 6 contact hours per week in the class, there will be no formal office hours. However, private help can be scheduled.

Final Exam: There will be no final exam.

Disabilities: If you have any condition such as a physical or learning disability, which will make it difficult for you to carry out the work as I have outlined it or which will require academic accommodations, please notify me and contact the Disabled Students Office (Holton 202), in the first two weeks of the course.

Academic Honesty: (The following note appears at the insistence of the administration) Plagiarism and cheating are serious offenses and may be punished by failure on the exam, paper or project; failure in the course; and/or expulsion from the University. For more information refer to the “Academic Dishonesty” policy in the K-State Undergraduate Catalog and the Undergraduate Honor System Policy on the Provost’s web page at [http://www.ksu.edu/facsen/policy/honorcod.htm](http://www.ksu.edu/facsen/policy/honorcod.htm)